MAIL ROOM
JUN 10 1994
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#20/ Formal Drawing
R. Morgan
6/14/94

780.29767X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Thomas J. CAMPANA, Jr., et al.
Serial No.:	07/702,938
Filed:	May 20, 1991
For:	SYSTEM FOR INTERCONNECTING ELECTRONIC MAIL SYSTEMS BY RF COMMUNICATIONS AND METHOD OF OF OPERATION THEREOF
Group:	2608
Examiner:	G. Oehling

TRANSMITTAL OF FORMAL DRAWINGS

June 10, 1994

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Sir:

Enclosed are twelve (12) sheets of formal drawings, showing Figs. 1-12, in connection with the above-identified application.

Respectfully submitted,

Donald E. Stout

Donald E. Stout
Registration no. 26,422

DES/bt
(202) 828-0300
attachments

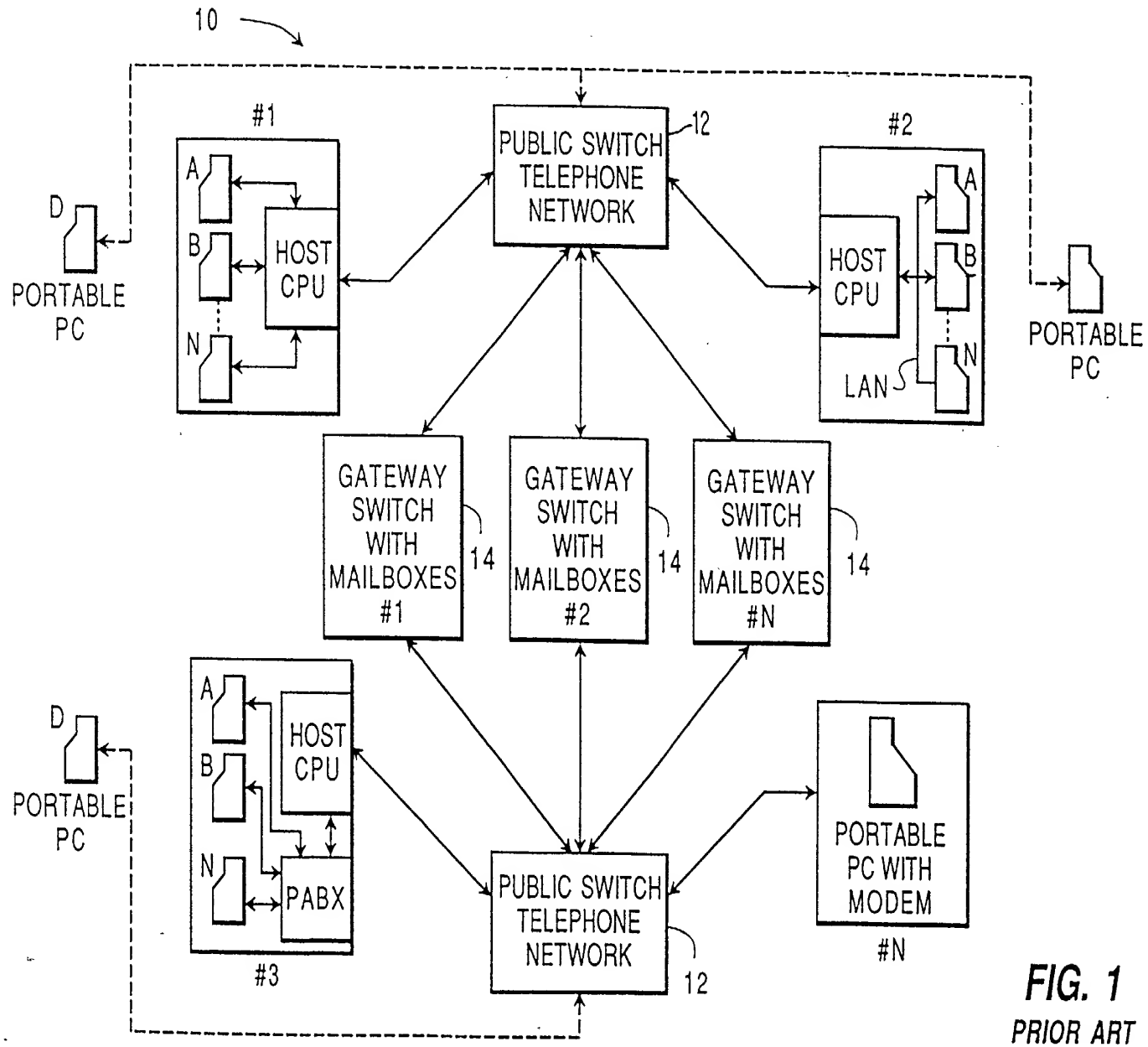


FIG. 1
PRIOR ART

CLASS SUBCLASS

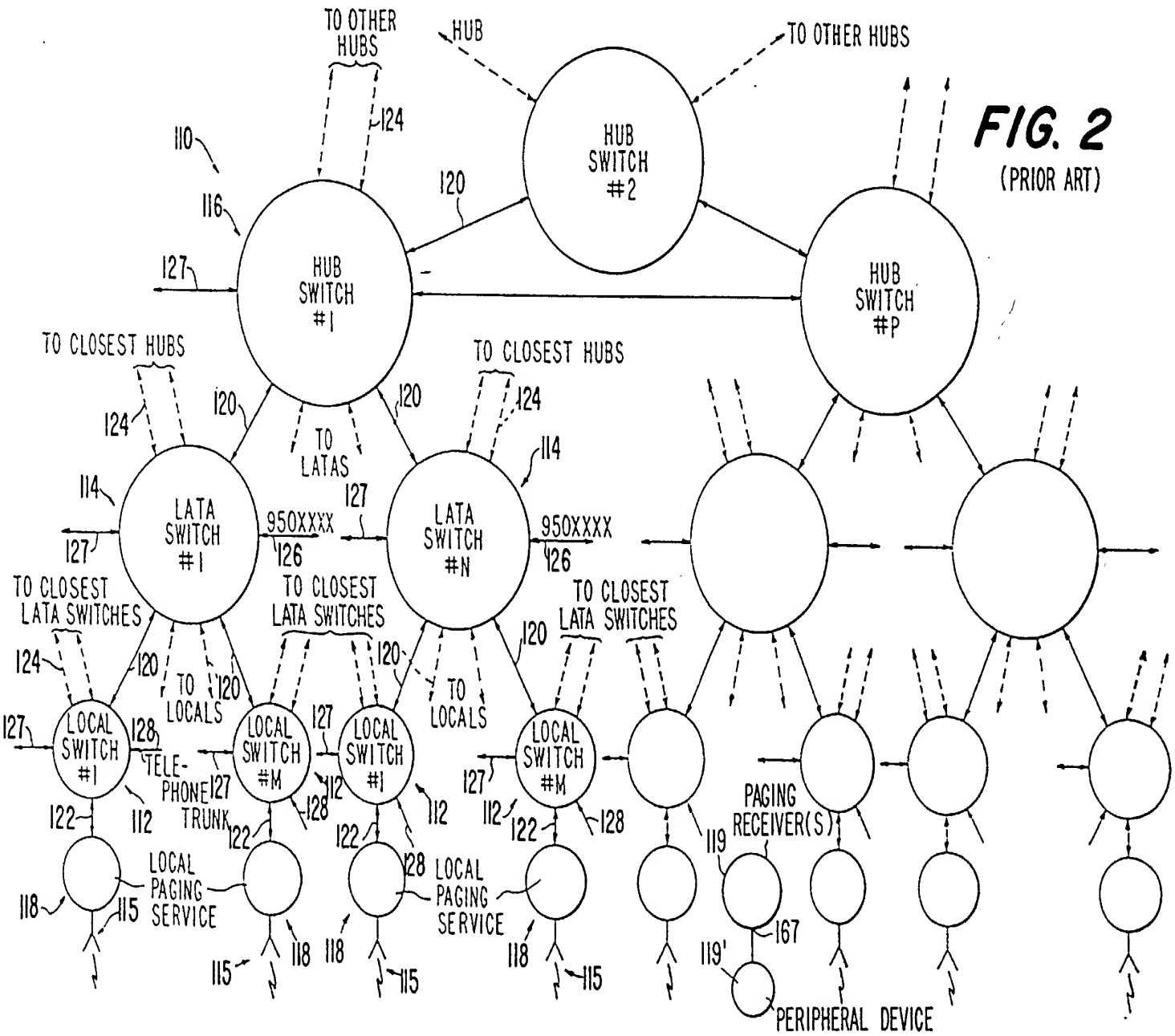
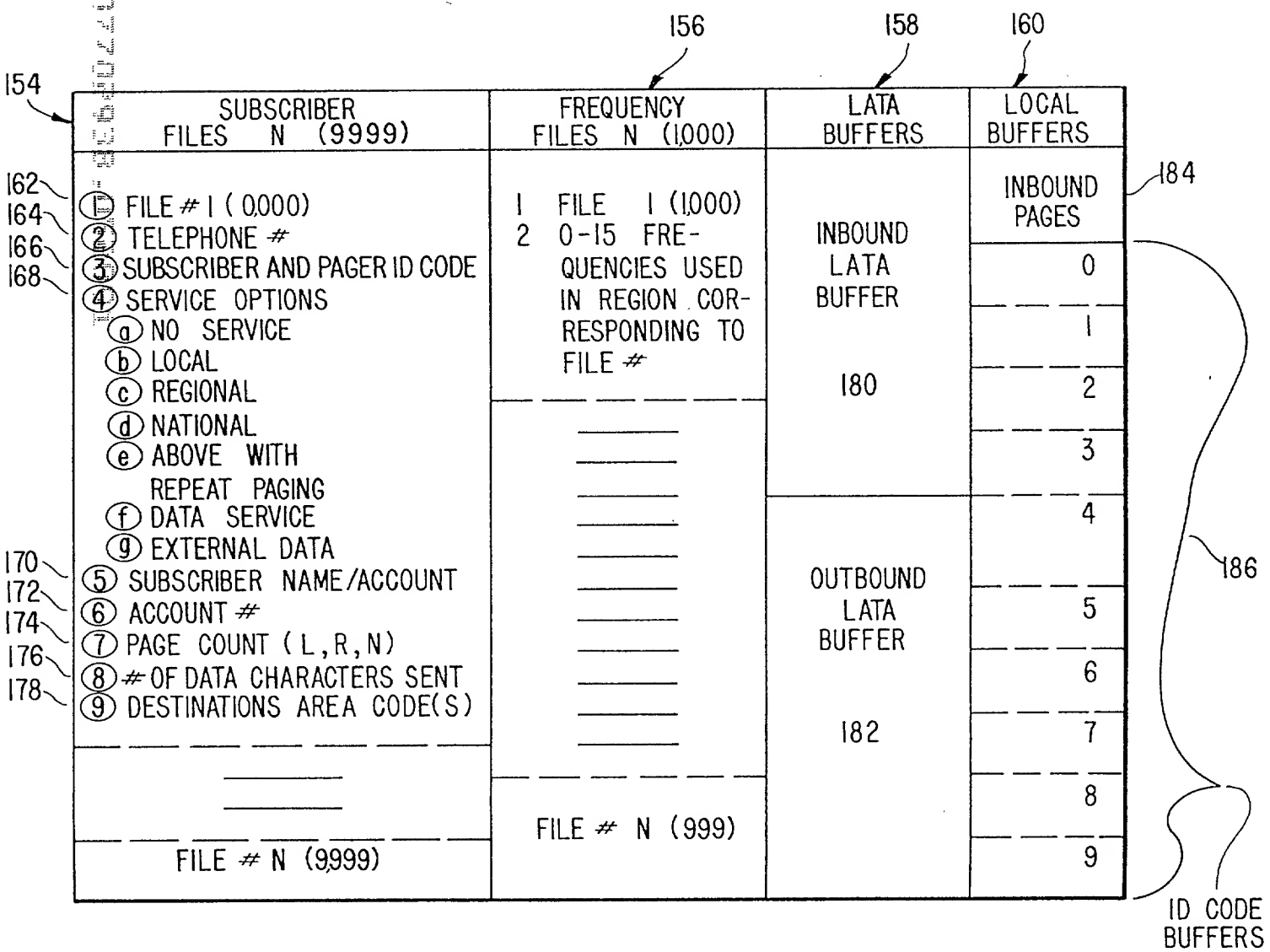


FIG. 2
(PRIOR ART)

G. FIG.
DRAFTSMAN

FIG. 3
(PRIOR ART)
LOCAL SWITCH MEMORY MAP



G. FIG.
SUBCLASS

FIG. 4

(PRIOR ART)

LATA SWITCH MEMORY MAP

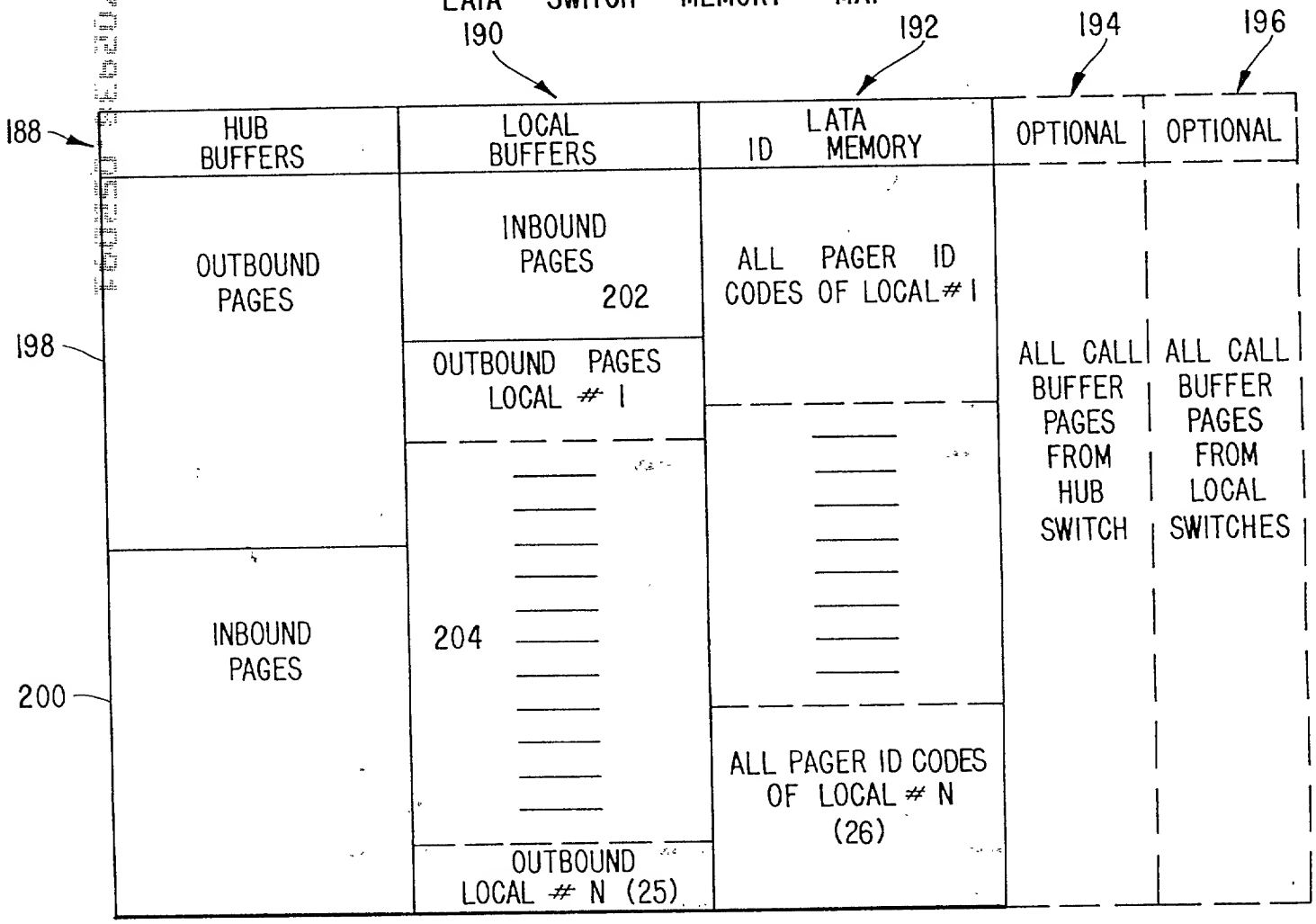
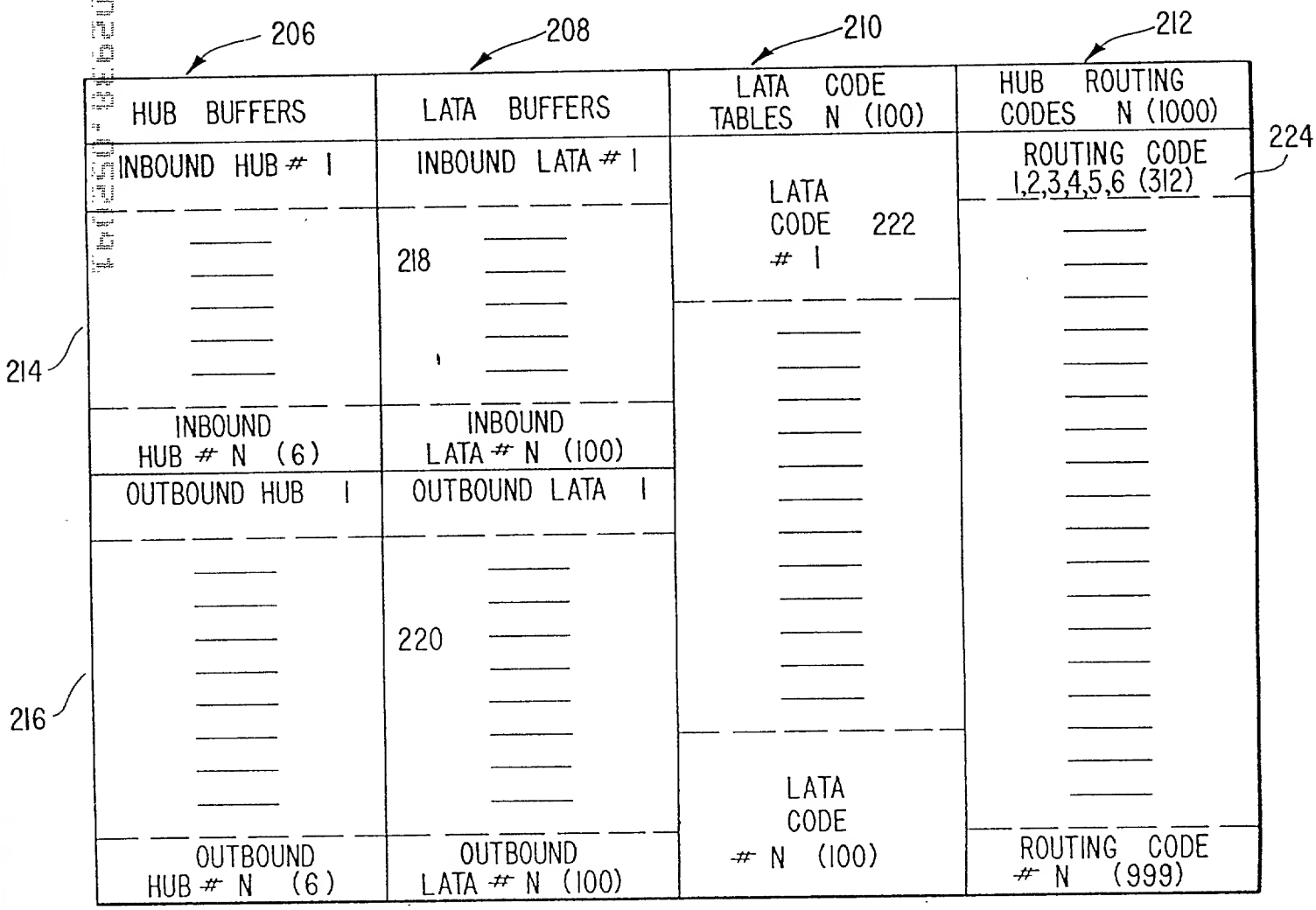


FIG.
CLASS SUBCLASS

FIG. 5
(PRIOR ART)
HUB SWITCH MEMORY MAP

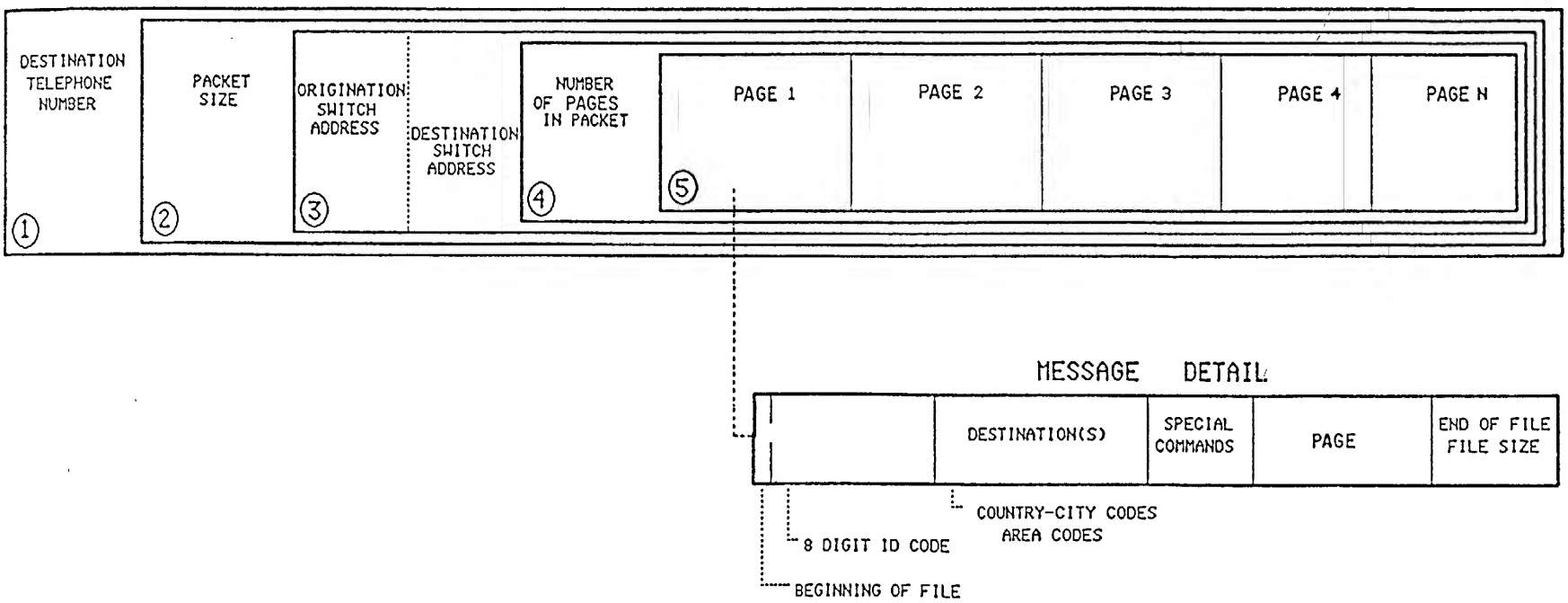


EP 0 262 262 A2

FIG. FIG. 6 (PRIOR ART)

FIG. 6
(PRIOR ART)

THE FIVE LAYER MODIFIED X.25 PACKET



G. FIG.
CLASS SUBCLASS

FIG. 7
PRIOR ART

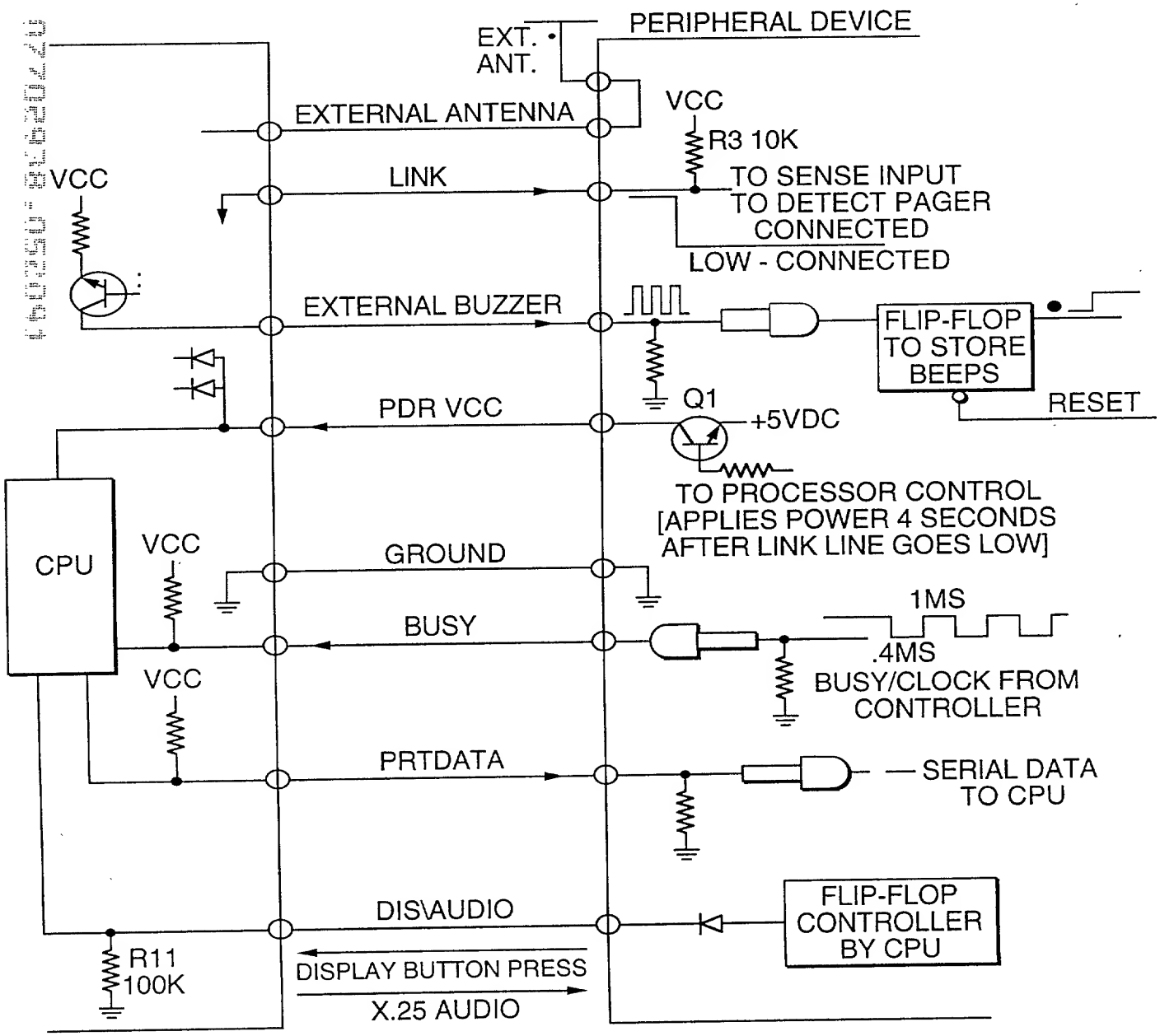


FIG. 8

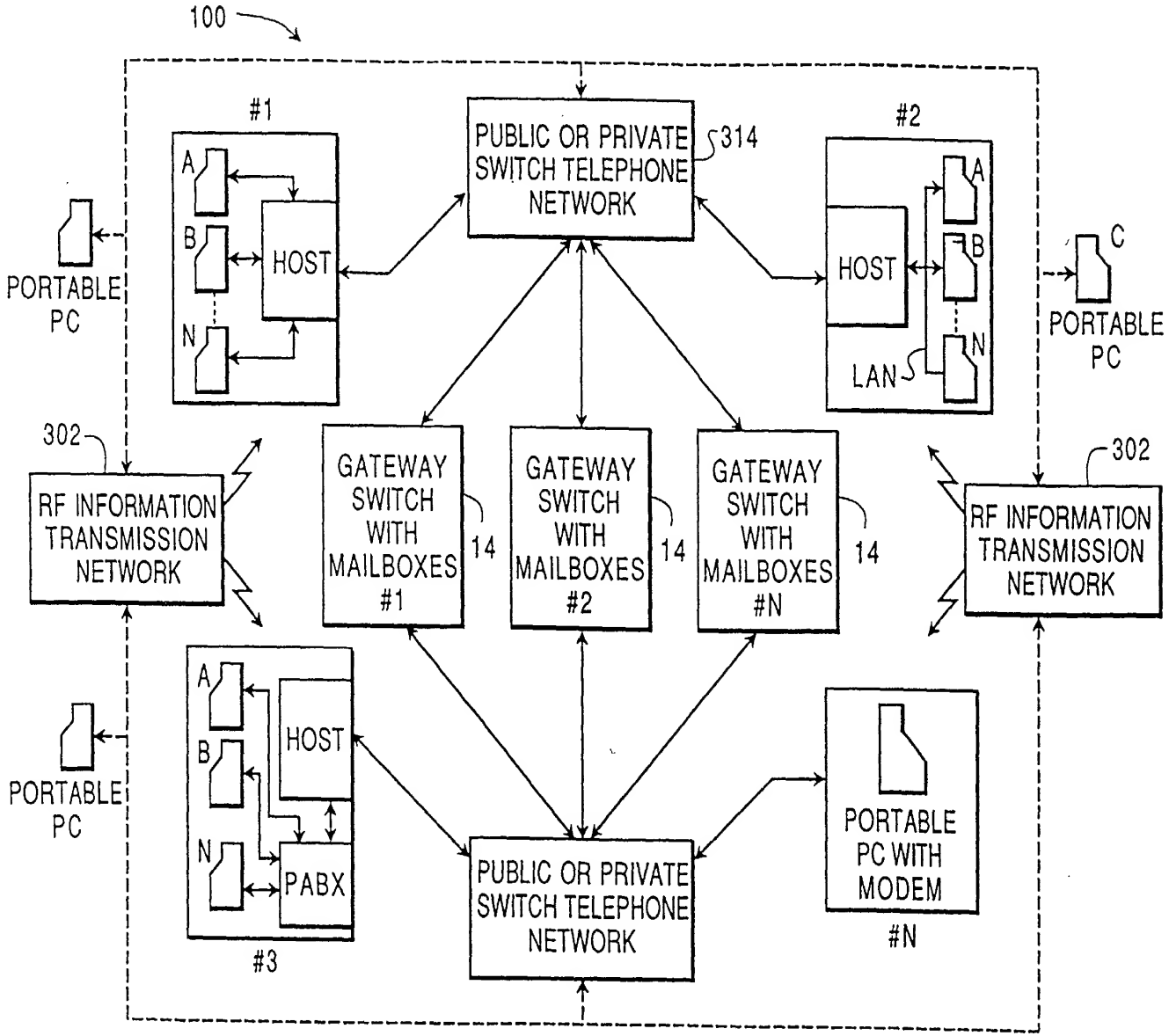

100
#1
A
B
N
HOST
PUBLIC OR PRIVATE SWITCH TELEPHONE NETWORK
314
#2
A
B
N
HOST
LAN
C
PORTABLE PC
PORTABLE PC
302
RF INFORMATION TRANSMISSION NETWORK
GATEWAY SWITCH WITH MAILBOXES #1
14
GATEWAY SWITCH WITH MAILBOXES #2
14
GATEWAY SWITCH WITH MAILBOXES #N
14
302
RF INFORMATION TRANSMISSION NETWORK
PORTABLE PC
A
B
N
HOST
PABX
#3
PUBLIC OR PRIVATE SWITCH TELEPHONE NETWORK
PORTABLE PC WITH MODEM
#N

FIG. 9

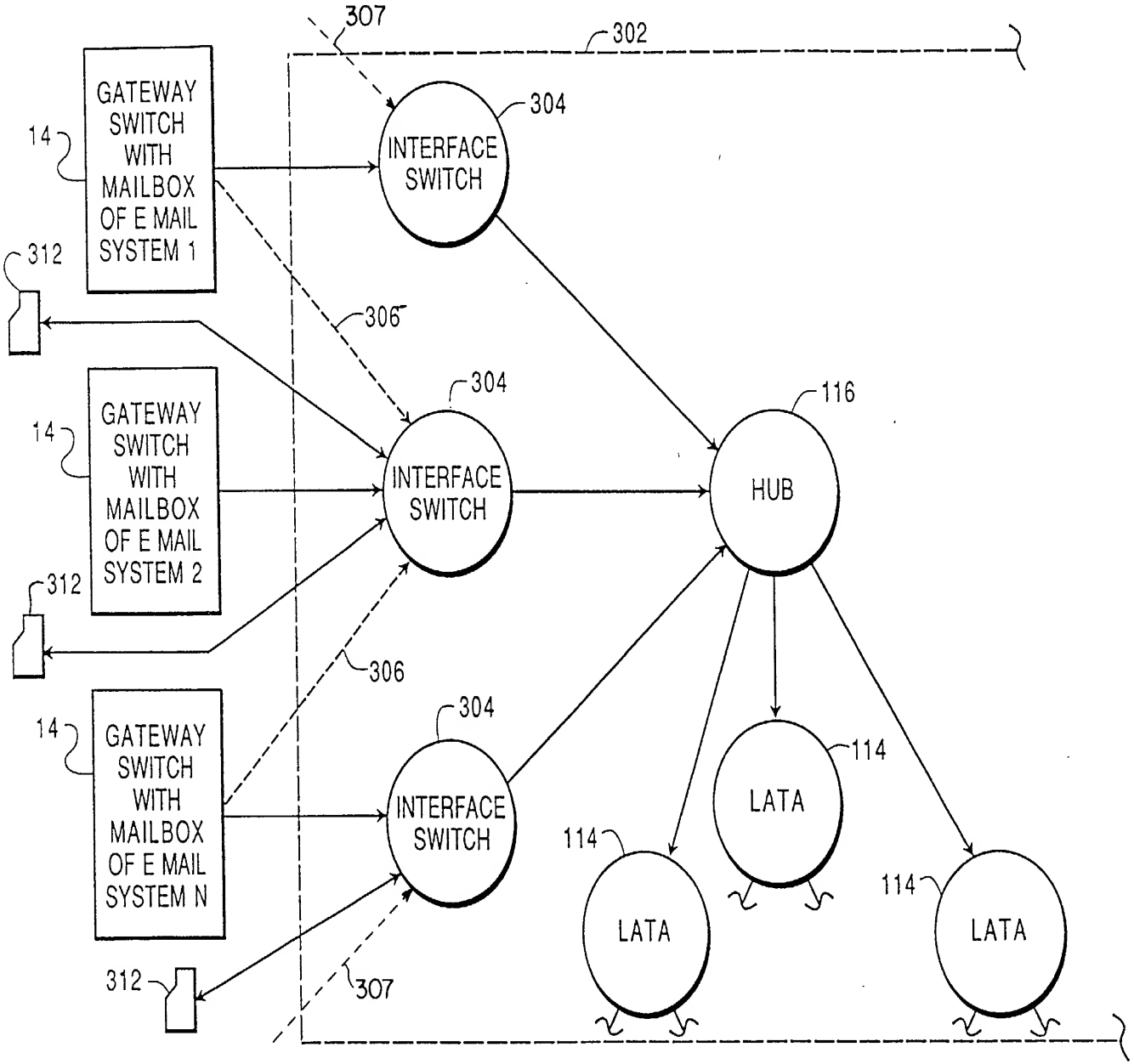

307
302
GATEWAY SWITCH WITH MAILBOX OF E MAIL SYSTEM 1
14
INTERFACE SWITCH
304
312
306
GATEWAY SWITCH WITH MAILBOX OF E MAIL SYSTEM 2
14
304
INTERFACE SWITCH
116
HUB
312
306
GATEWAY SWITCH WITH MAILBOX OF E MAIL SYSTEM N
14
304
INTERFACE SWITCH
114
LATA
114
LATA
114
LATA
312
307

FIG. 10

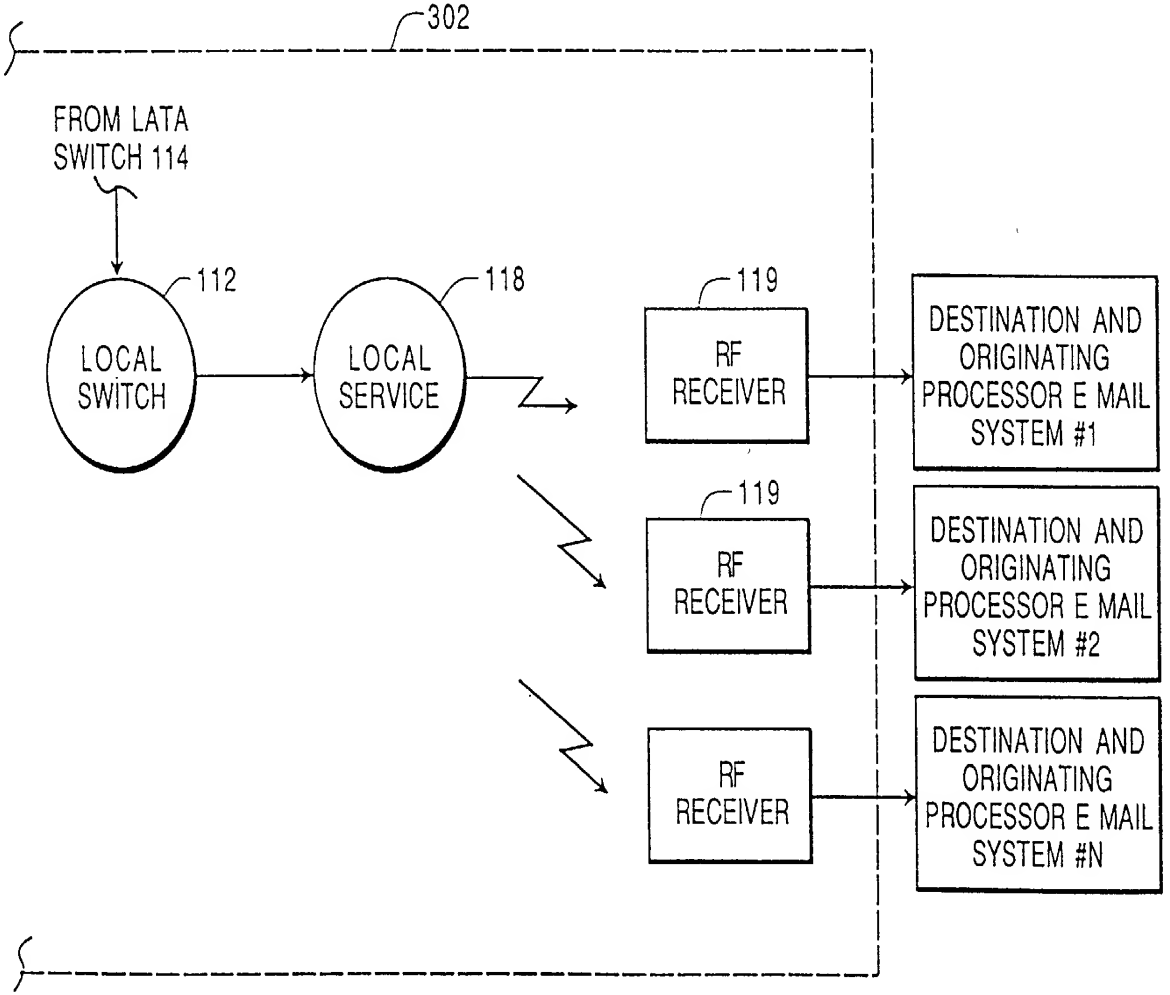
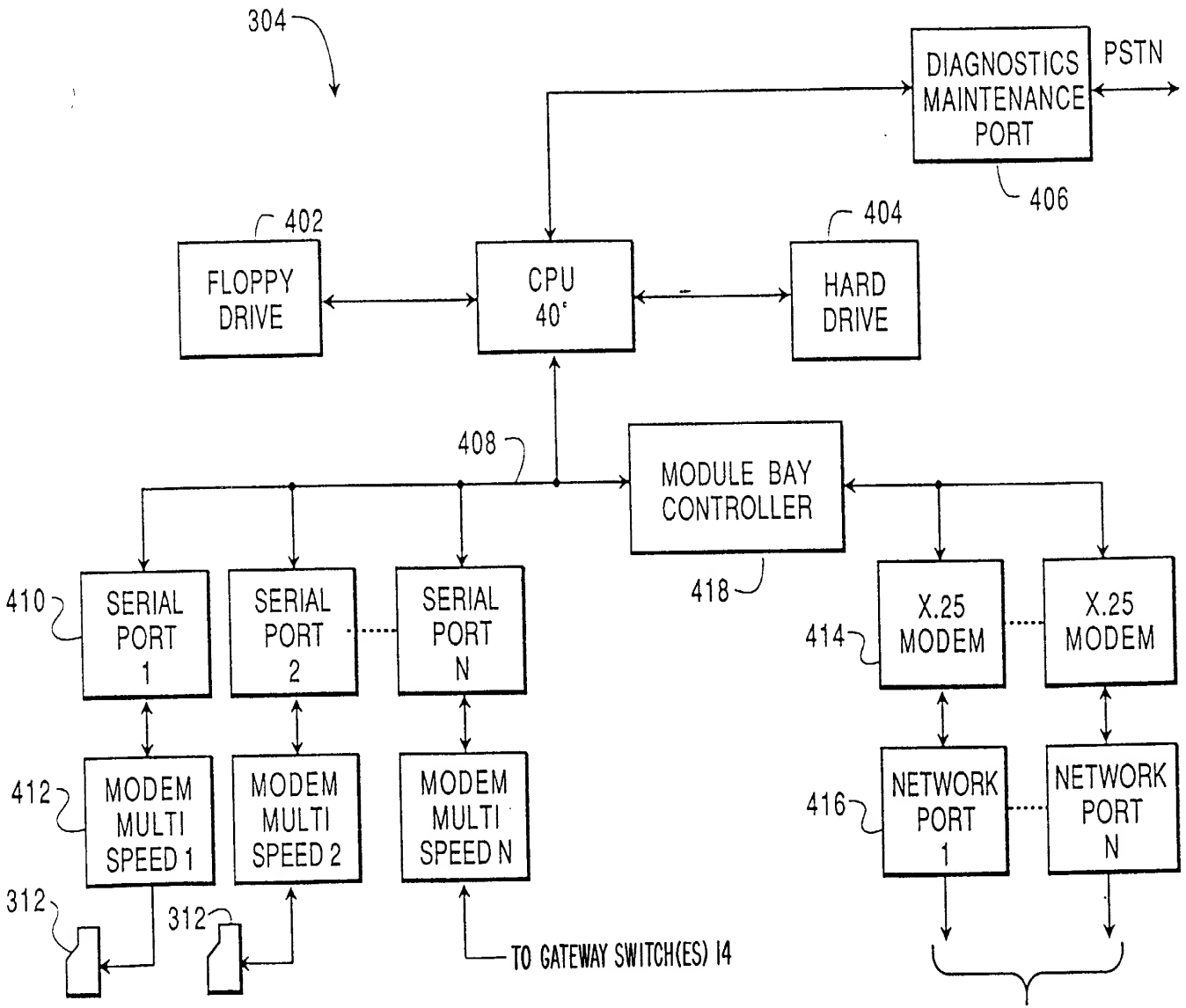

302
FROM LATA SWITCH 114
112
LOCAL SWITCH
118
LOCAL SERVICE
119
RF RECEIVER
119
RF RECEIVER
RF RECEIVER
DESTINATION AND ORIGINATING PROCESSOR E MAIL SYSTEM #1
DESTINATION AND ORIGINATING PROCESSOR E MAIL SYSTEM #2
DESTINATION AND ORIGINATING PROCESSOR E MAIL SYSTEM #N

FIG. 11

ENTRY METHOD	ORIGINATING PROCESSOR	GATEWAY SWITCH 14	INTERFACE SWITCH 304
1	ADDS INTERFACE (WIRELESS) DESTINATION AND DESTINATION PROCESSOR	NO-ACTION	ADDS ID OF RF RECEIVER 119
2	ADDS INTERFACE (WIRELESS) DESTINATION AND ID OF RECEIVER 119	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION
3	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION	ADDS ID OF RECEIVER 119
4	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
5	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	ADDS ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
6	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	NO-ACTION	ADDS ID OF RECEIVER 119
7	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119(BY COMPARING DESTINATION PROCESSOR TO ID TABLE).	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION

FIG. 12


304
DIAGNOSTICS MAINTENANCE PORT
PSTN
406
402
FLOPPY DRIVE
CPU 40'
404
HARD DRIVE
408
MODULE BAY CONTROLLER
418
410
SERIAL PORT 1
SERIAL PORT 2
SERIAL PORT N
412
MODEM MULTI SPEED 1
MODEM MULTI SPEED 2
MODEM MULTI SPEED N
312
312
TO GATEWAY SWITCH(ES) 14
414
X.25 MODEM
X.25 MODEM
416
NETWORK PORT 1
NETWORK PORT N